



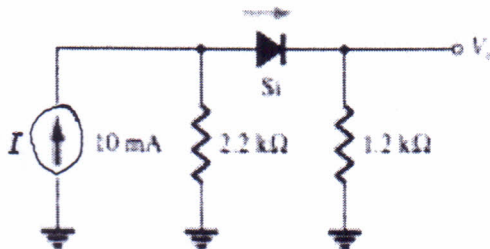
Electronics 1
Course Code: ECE 261
Fall Semester Exam.



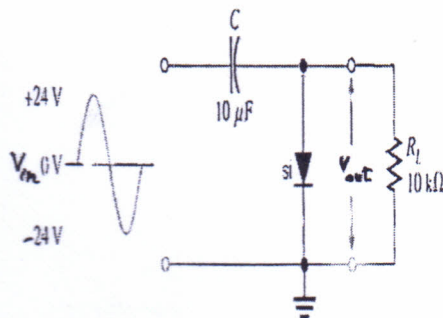
BME Program - Level 200
Exam Date: 25-11- 2018
Allowed Time: 1 Hour

Attempt five questions only. Assume any missed data. Full mark is 20.

Q.1) Determine V_o for the DC configuration shown below. [4 Marks]

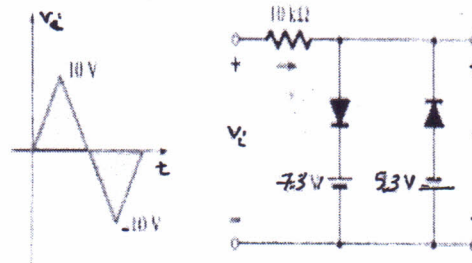


Q.3) Find and sketch V_o for the circuit shown. [4 Marks]

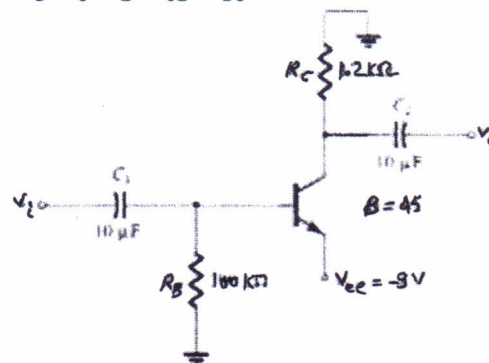


Q.5) Design a voltage regulator that will maintain an output voltage of 20 V across a 1 kΩ load with an input that will vary between 30 and 50 V. [4 Marks]

Q.2) Determine and sketch V_o for the configuration shown below. [4 Marks]



Q.4) For the BJT circuit shown, find $I_B, I_C, I_E, V_{CE}, V_{BC}$ [4 Marks]



Q.6) Using neat sketches **only**, compare between the output characteristics for the common-base configuration and the common emitter configuration. Define the parameters α and β . [4 Marks]

☺My best wishes to all of you☺

Assoc. Prof. Hossam El-Din Moustafa