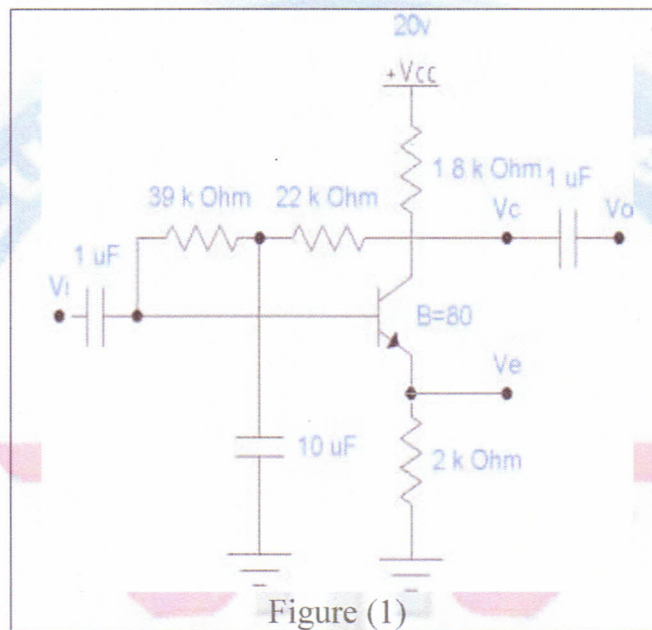


BME – Level 200		Electronics(2) - ECE264 Time Allowed : 60 minutes Total Mark : 20 Marks
Name : Number:		
Midterm Exam 2016/2017 – 2 nd semester		
ملحوظات: 1- الامتحان مكون من (4 صفحات : صفحة اسئلة و 3 صفحات للاجابة) ولا يسمح بالاجابة في اي ورق خارجي		
2- يجب كتابة اسم الطالب في المكان المخصص للاسم		

1- For the Circuit Shown in Figure 1; (a) For the DC-analysis : find I_B , I_C , I_E , V_C , V_E , V_{CE} , and the thermal stability (b) for the AC analysis; using simplified hybrid model : find voltage gain , current gain , input impedance , output impedance , and obtain whether there is a phase shift or not [7 Marks]



2- Compare between JFET, and BJT Technically, and for fabrication. According to your comparison; as Engineer: which of them you prefer in circuits Design. [6 Marks]

3- For the Circuit Shown in Figure 2; (a) For the DC-analysis: find I_D , I_S , I_B , V_{DS} , V_{GS} , Mode of operation. (b) for the AC analysis: find voltage gain , current gain , input impedance , output impedance , and obtain whether there is a phase shift or [7 Marks]

